## PRELIMINARY PRODUCT BRIEF

μ**PD720110**

UNIVERSAL SERIAL BUS 2.0 HUB CONTROLLER

The μPD720110 integrates hub controller, transaction translator and USB 2.0 transceivers into one single chip. With vendor ID implemented in external ROM, application specific compound hub such as premium keyboard can be easily designed. This provides a total hub solution for hub/peripherals manufacturers. This hub enhances user experience for high-performance multimedia peripherals such as DVD-ROM drives, video cameras, scanners, printers and internet connections running in USB 2.0 while USB 1.1 applications such as keyboard and mouse will be speed-shifting into USB 2.0 speed and running concurrently in achieving optimal performance throughput.

FEATURES

- Compliant with Chapter 11 of USB Specification Revision 2.0 (Data Rate 1.5/12/480 Mbps)
- Support split transaction by transaction translator
- On-chip USB request processing sequencer for Hub function
- Support Self-powered mode
- 4 downstream ports
- Over current control circuit in the chip.
- Selectable for port power control (individual or gang)
- Selectable non-removable attribution support
- Support External ROM IF for Descriptor
- System Clock is generated by 30 MHz X'tal
- 3.3 V Power supply. 5 V tolerant for some inputs.

BLOCK DIAGRAM
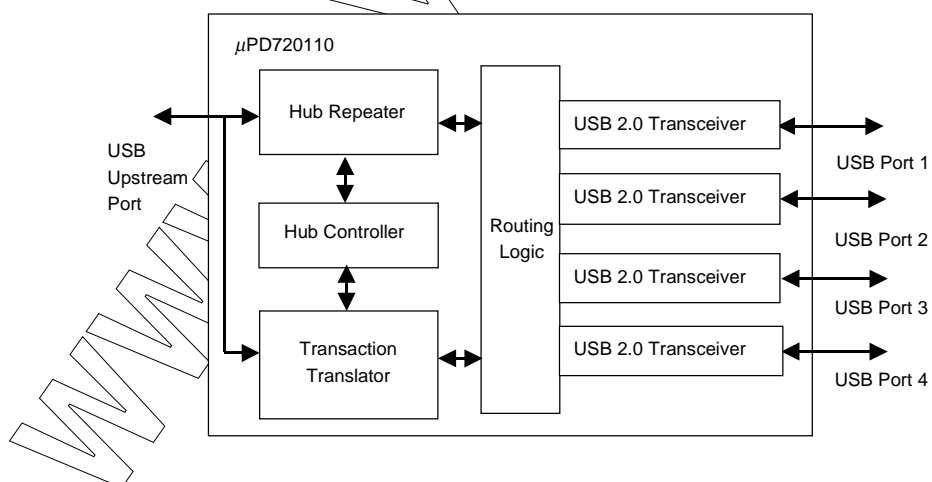
PACKAGE

- 100-pin plastic LQFP

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