# Preliminary HFD3041-102

## FEATURES

- TO-46 hermetic package
- Data rates from DC to 2.5Gbps InGaAs PIN detector and BiCMOS preamplifier
- Operation at 3.3V
- Differential output for low noise
- 2.3GHz typical Bandwidth



The HFD3041-102 is a high-performance 1300nm integrated InGaAs detector (80micron active area) and pre-amplifier TO-46 hermetic component, the product is designed for ease of use in modules designed for 2.5GB/s data rate.

The HFD3041-102 converts optical power into a differential output electrical signal that is used in fiber optic communications and other applications. As the light increases, the differential output voltage increases. Above peak optical powers of approximately –12dBm, AGC circuitry in the TIA limits the optical voltage swings. The differential output is designed to be **AC** coupled to a 50 ohm load impedance pulled down to ground prior to any post amplification stages. The component requires alignment in a lens system which focuses the light onto the photodiode active area.

To achieve the full specified operational bandwidth, it is recommended that the products component leads at attached to within 0.10" of the receptacle with a controlled impedance path.

The Honeywell HFD3041-102 is designed to interface with 50/125 and 62.5/125mm multimode fiber.

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## ABSOLUTE MAXIMUM RATINGS

| Parameter                  | Rating                     |
|----------------------------|----------------------------|
| Storage Temperature        | -40 to +85°C               |
| Case Operating Temperature | -40 to +85°C               |
| Lead Solder Temperature    | 260°C, 10 sec.             |
| Power Supply Voltage       | -0.5V to 3.8V              |
| Incident Optical Power     | 0 dBm average, +4 dBm peak |

# NOTICE

Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operations section for extended periods of time may affect reliability.

#### ELECTRO-OPTICAL CHARACTERISTICS (Vcc=3.3V, AC coupled to 50Q, 0°C<T<70°C unless otherwise specified)

| Parameters                                   | <b>Test Condition</b>   | Symbol                      | Min. | Тур. | Max. | Units      | Notes |
|--|---|-----------------------------|------|------|------|------------|-------|
| Active Area                                  |   |                             |      | 80   |      | μm         |       |
| Input Optical Wavelength                     | 0°C to 70°C   | $\lambda_{\rm P}$           | 1200 | 1310 | 1600 | nm         |       |
| Responsivity                                 | $P_R < AGC_{th}$ , -40°C to +85 °C<br>PD active area is 80um dia. | R                           | 1.3  | 2.7  | 4.2  | $mV/\mu W$ | 1,2,7 |
| Differential Output<br>Voltage Swing         | $P_R = -7 dBm$ , AC Coupled to $R_L = 50 \Omega$                  | V <sub>o(pk-pk)</sub>       | 0.10 | 0.20 |      | V          |       |
| Supply Current                               | $P_R = 0\mu W$ peak, $R_L = 50\Omega$                             | ICC                         |      | 25   | 40   | mA         | 1     |
| -3dB Optical/Electrical<br>Bandwidth         | $P_R = -12 dBm$<br>Temp = 25°C                                    | BW                          | 1.6  | 1.8  | 2.5  | GHz        | 1,3   |
| Low Frequency –3dB<br>Cutoff                 | $P_R = -12 dBm$   | $\mathrm{BW}_{\mathrm{LF}}$ |      |      | 0    | KHz        | 1,3   |
| RMS Input Referred<br>Noise Equivalent Power | 1875 MHz, 4-pole BT Filter,<br>P <sub>R</sub> =0uW (Dark)         | NEP                         |      | 0.50 | 0.65 | μW         | 4     |
| Power Supply Rejection<br>Ratio              | $P_R = 0\mu W$ (Dark), Freq = 100MHz                              | PSRR                        |      | 30   |      | dB         | 1,8   |
| Pulse Width Distortion                       | $P_R = -12 dBm$   | PWD                         |      |      | 60   | ps         | 1,5   |
| Rise/Fall Time                               | $P_{R} = -12 dBm, (20\% - 80\%)$                                  | $T_R/T_F$                   | 105  | 150  | 170  | ps         | 1,6,7 |
| AGC Threshold Power                          | Peak Optical Power Output   | AGC <sub>th</sub>           |      | 65   |      | μW         | 9     |

#### Notes:

- 1.  $P_R$  is the average optical power incident on the component window, subject to note 7.
- 2. Responsivity measured with source wavelength of 1310nm, with light source modulated at 250MHz. Peak received optical power  $<\!AGC_{th}$
- 3. Bandwidth is measured with a small signal sinusoidal light source with -12dBm average power
- RMS input referred optical noise equivalent power is obtained by measuring the RMS output noise into an 1875 MHz, 4-pole Bessel-Thompson filter then dividing by the responsivity.
- 5. Measured at the 50% level of output pulses.
- Rise/Fall times are corrected for optical source Rise/Fall times. The corrected value is calculated as the square root of the difference of the squares of the measured differential detector output and the source.
- 7. Unless otherwise stated, all output parameters are measured differentially using an optical lens which focuses the optical power within a spot diameter smaller than the photodiode active area. Customers results will depend on the optical lens system used. Component leads are shorter than 0.1 inch in length.

- Value shown is with no external power supply filtering. Improved performance can be obtained by using external filtering close to the power supply leads.
- 9. The AGC threshold power is the peak received optical power. At lower power, the receiver operates in its linear responsivity characteristic region. Above AGC threshold, the output voltage is relatively independent of the optical input power.
- 10. Typical values represent measured data at 25°C.

## NOTICE

The inherent design of this component causes it to be sensitive to electrostatic discharge (ESD). To prevent ESD-induced damage and/or degradation to equipment, take normal ESD precautions when handling this product

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## FIGURE 1: INTERNAL SCHEMATIC DIAGRAM OF THE HFD3381-102



## FIGURE 2: RECOMMENDED INTERFACE CIRCUIT FOR THE HFD3381-102



Note:  $100\Omega$  terminating resistor is optional

### R=10 Ω

 $C_1 = 10 \text{ nF}$ 

 $C_2$  = Data rate dependant (22nF for rates > 1Gbps)

### **ORDER GUIDE**

| Catalog Listing | Description                            |
|-----------------|--|
| HFD3041-102     | PIN Plus Preamplifier, TO-46 Component |

### MOUNTING DIMENSIONS (for reference only) in./(mm)



#### **PINOUT**

| Number | Function            |
|--------|---------------------|
| 1      | V <sub>CC</sub>     |
| 2      | Inverted Output     |
| 3      | Ground              |
| 4      | Non-Inverted Output |

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### Preliminary

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