# Si PIN photodiode with preamp **S6468 series**



## High-speed sensor with preamp

S6468 series is a high-speed photodetector consisting of a Si PIN photodiode and a preamplifier chip integrated in the same package. They feature high-speed response and high sensitivity over a wide spectral range from visible to near infrared light. The small package (TO-18) allows compact optical design. The amplifier input is at a virtual ground, so external noise which may appear when detecting high-speed signals can be suppressed.

#### **Features**

Cut-off frequency (Vcc=5 V)
 S6468 : 15 MHz
 S6468-02: 35 MHz

Low noise (f=1 MHz)
 S6468 : 25 nVrms/Hz<sup>1/2</sup>
 S6468-02: 28 nVrms/Hz<sup>1/2</sup>

◆ 3 pin TO-18 package◆ Active area: ♦0.8 mm

## **Applications**

- Optical fiber communication
- Video signal transmission
- Optical disk pick-up



■ Electrical and optical characteristics [Ta=25 °C, Vcc=5 V, R↓=500 Ω), 6≥=13 pF] \*1

Parameter	Symbol	Condition	S6468			S6468-02			1.1
			Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
Spectral response range	λ		/320 to 1060		320 to 1000			nm	
Peak sensitivity wavelength	λρ		-	900	-	-	800	-	nm
Photo sensitivity	S	λ=660 nm	_	13.5	1	-	8.5	-	mV/μW
		λ=780 nm	-	15.5	-	-	11	-	
		λ=830 nm	/ (- <	16.5	1	-	11	-	
Trans-impedance	Rт		-	30	-	-	20	-	kΩ
Power supply current	Icc	RL=∞		-	3	-	-	3	mA
Output bias voltage *2	Vo	R∟=∞ Pin=0 µW	0.55	0.65	0.8	0.65	0.8	0.9	V
Temperature coefficient of output bias voltage	-		7/	-2	-	-	-2	-	mV/ °C
Cut-off frequency	fc	Pin=10 μW *3	12	15	-	28	35	-	MHz
Maximum output voltage amplitude	-	Nonlinear distortion: √10 % Max.	0.5	-	-	0.5	-	-	Vp-p
Output impedance	Zo	f=5 Hz	-	30	-	-	30	-	Ω
Output noise voltage	Vn	Pin≥0 µW f=1 MHz	-	25	-	-	28	-	nV/Hz <sup>1/2</sup>
Overshoot	-	Pin=10 μW *3	_	-	10	-	-	10	%

#### ■ Absolute maximum ratings

Parameter	Symbol	Min.	Max.	Unit
Power supply voltage *4	Aec	-0.5	7	V
Power dissipation	Р	-	300	mW
Operating temperature	√Topr	-20	70	°C
Storage temperature	Tstg	-40	100	°C

■ Recommended operating conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit
Power supply voltage	Vcc	4.75	5	5.25	V
Load resistance	R∟	500	-	-	Ω
Load capacitance	CL	-	-	13	pF
Operating temperature	Topr	0	ı	60	°C

<sup>\*4:</sup> A bypass capacitor (0.01  $\mu$ F to 0.1  $\mu$ F ceramic) is connected between the Vcc lead and the GND lead. The lead length should be less than 20 mm.





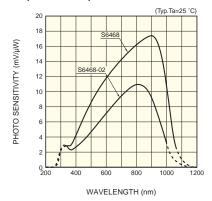
<sup>\*1:</sup> For definitions of RL and CL, refer to the basic connection.

<sup>\*2:</sup> Output voltage Vout =Vo-(Pin  $\times$  S) Pin: incident radiant flux ( $\mu$ W)

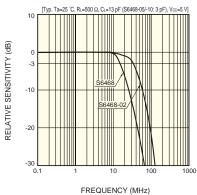
<sup>\*3:</sup> Peak value

KPINB0126EB

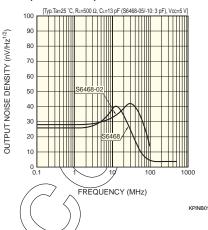
### ■ Spectral response



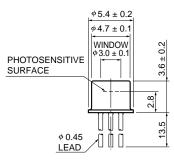
### ■ Frequency characteristics



#### ■ Output noise spectrum



#### ■ Dimensional outline (unit: mm)

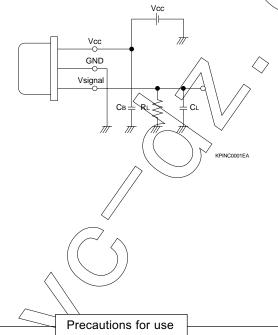




② GND (CASE) ③ V signal

KPINAMM1F

## Basic connection



S6468 series may be damaged or their performance may deteriorate by such factors as electro static discharge from the human body, surge voltages from measurement equipment, leakage voltages from soldering irons and packing materials, etc. As a countermeasure against electro static discharge, the device, operator, work place and measuring jigs must all be set at the same potential. The following precautions must be observed during use:

- · To protect the device from electro static discharge which accumulate on the operator or the operator's clothes, use a wrist strap or similar tools to ground the operator's body via a high impedance resistor (1 M $\Omega$ ). A semiconductive sheet (1 M $\Omega$ ) to 100 M $\Omega$ ) should be laid on both the work table and the floor in the work area.
- When soldering, use an electrically grounded soldering iron with an isolation resistance of more than 10 MΩ.
- For containers and packing, use of a conductive material or aluminum foil is effective. When using an antistatic material, use one with a resistance of 0.1 M $\Omega$ /cm<sup>2</sup>/to 1  $\Omega$ /cm<sup>2</sup>

- RL and CL are total resistive load and capacitive load viewed from the V signal terminal. When connecting a cable or circuit to the latter stage of the basic connection diagram, the cable or circuit resistance and capacitance should also be taken into account. They should be used in accordance with the recommended operating conditions: RL≥500 Ω and CL ≤ 13 pF.
- A bypass capacitor (Cβ≥0.01 μF to 0.1 μF ceramic) is connected between the Vcc lead and the GND lead.
- The lead length should be less than 20 mm.
- · If electric current or voltage is applied in reverse polarity to an electronic device such as a preamplifier, this can degrade device performance or destroy the device. Always check the wiring and dimensional outline to avoid misconnection.

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