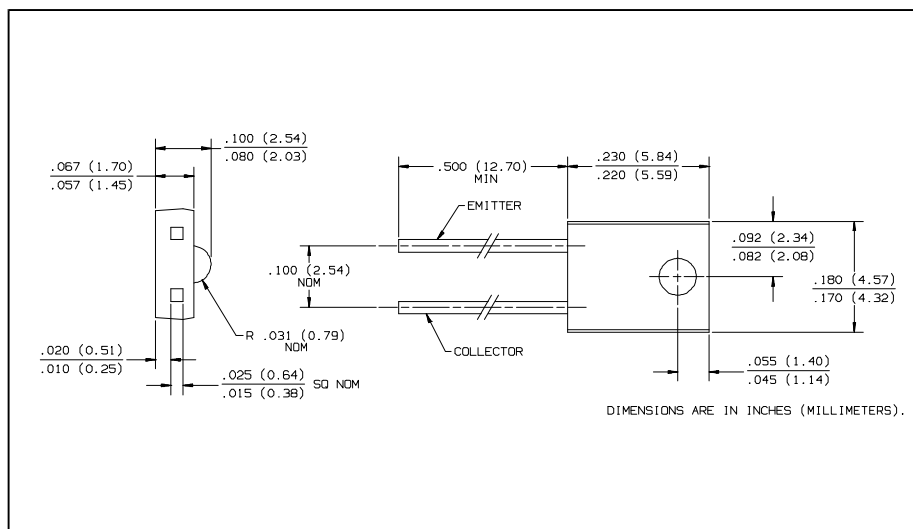
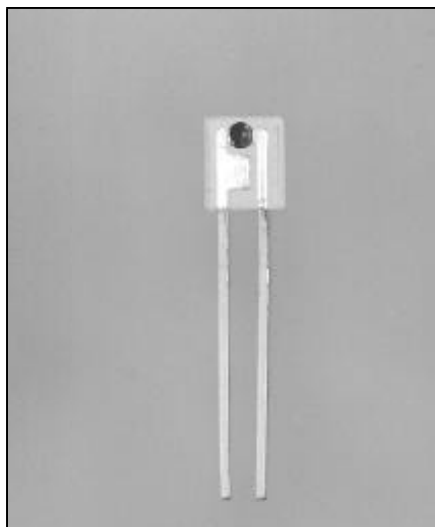


NPN Phototransistor with Base-Emitter Resistor Types OP750A, OP750B, OP750C, OP750D



Features

- Wide receiving angle
- Variety of sensitivity ranges
- Side-looking package for space limited applications
- Base-emitter resistor provides ambient light protection

Description

The OP750 series devices consist of an NPN silicon phototransistor molded in a clear epoxy package. The wide receiving angle provides relatively even reception over a large area. The side-looking package is designed for easy PC board mounting of slotted optical switches or optical interrupt detectors. This series is mechanically and spectrally matched to the OP140 and OP240 series of infrared emitting diodes.

The phototransistor has an internal base-emitter resistor which provides protection from low level ambient lighting conditions. This feature is also useful when the media being detected is semi-transparent to infrared light in interruptive applications.

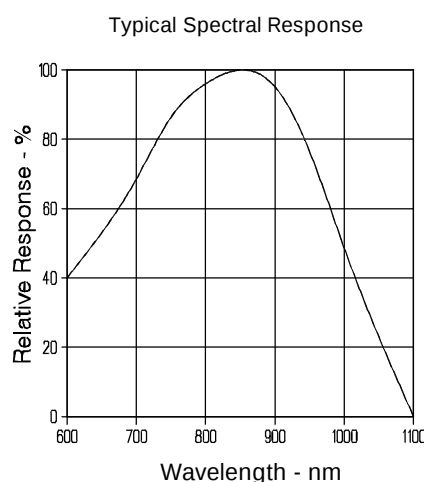
Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Collector-Emitter Voltage	30 V
Emitter Reverse Current	10 mA
Collector DC Current	30 mA
Storage and Operating Temperature Range	-40°C to $+100^\circ\text{C}$
Lead Soldering Temperature [1/16 inch (1.6 mm) from case for 5 sec. with soldering iron]	$260^\circ\text{C}^{(1)}$
Power Dissipation	$200\text{ mW}^{(2)}$

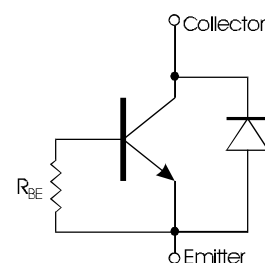
Notes:

- (1) RMA flux is recommended. Duration can be extended to 10 sec. max. when flow soldering. Max. 20 grams force may be applied to leads when soldering.
- (2) Derate linearly $1.33\text{ mW}/^\circ\text{C}$ above 25°C .
- (3) Light source is an unfiltered GaAs LED with a peak emission wavelength of 935 nm and a radiometric intensity level which varies less than 10% over the entire lens surface of the phototransistor being tested.
- (4) The knee point irradiance is defined as the irradiance required to increase $I_{C(ON)}$ to 50 μA .

Typical Performance Curves



Schematic



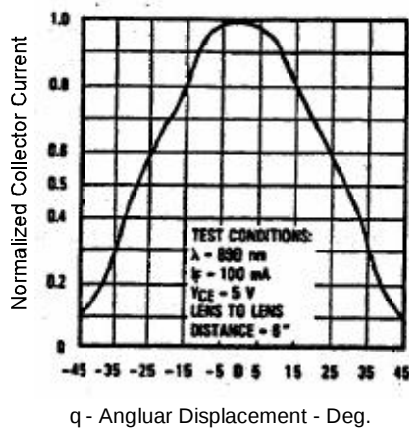
Types OP750A, OP750B, OP750C, OP750D

Electrical Characteristics ($T_A = 25^\circ \text{C}$ unless otherwise noted)

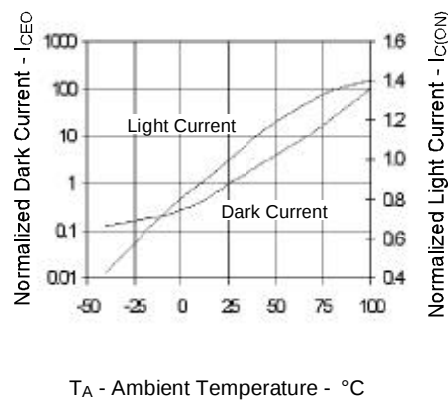
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
I _{C(ON)}	On-State Collector Current					V _{CE} = 5 V, E _e = 1 mW/cm ²⁽³⁾
	OP750A	2.25		7.00	mA	
	OP750B	1.50		4.20		
	OP750C	0.85		2.80		
	OP750D	0.85		7.00		
E _{KP}	Knee Point Irradiance		.03		mW/cm ²	V _{CE} = 5 V ⁽⁴⁾
I _{CEO}	Collector-Emitter Dark Current			100	nA	V _{CE} = 10 V, E _e = 0
I _{ECO}	Emitter-Reverse Current			100	μA	V _{EC} = 0.4 V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	30			V	I _C = 100 μA
V _{CE(SAT)}	Collector-Emitter Saturation Voltage			0.4	V	I _C = 100 μA, E _e = 1 mW/cm ²⁽³⁾

Typical Performance Curves

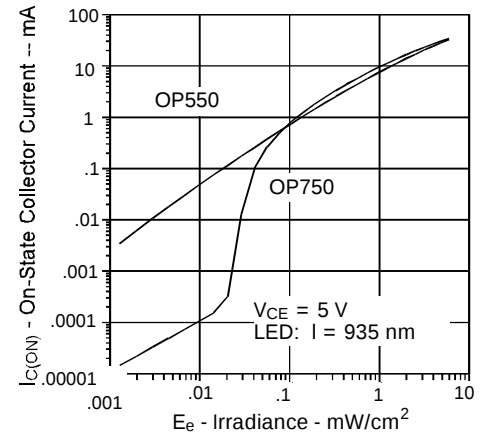
Normalized Collector Current vs. Angular Displacement



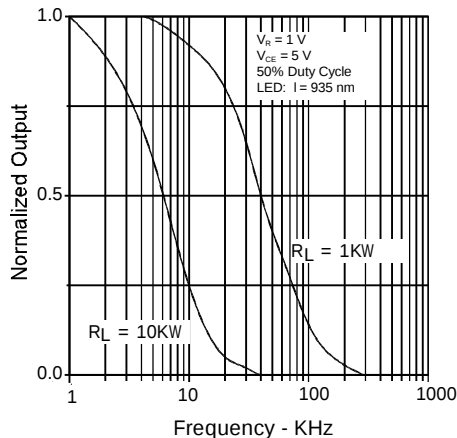
Normalized Light and Dark Current vs. Ambient Temperature



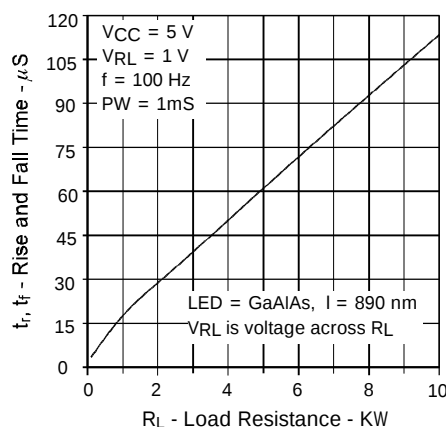
On-State Collector Current vs. Irradiance



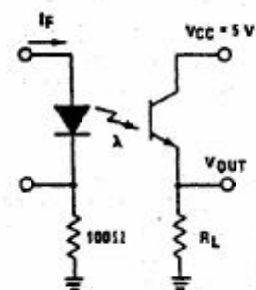
Normalized Output vs. Frequency



Typical Rise and Fall Time vs. Load Resistance



Switching Time Test Circuit



Test Conditions:
Light source is pulsed LED with t_r and $t_f \leq 500 \text{ ns}$.
 I_f is adjusted for $V_{OUT} = 1 \text{ Volt}$.

Optek reserves the right to make changes at any time in order to improve design and to supply the best product possible.

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