PHOTONIC DETECTORS INC.

High Speed Detector Amplifier Hybrid Type PDB-708



FEATURES

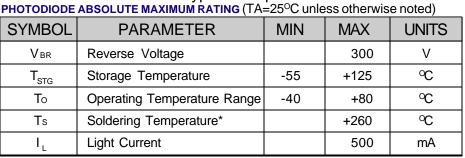
- 24 MHz bandwidth
- single supply operation
- Wide dynamic range
- Low power: 5 V @ 25 mA

The **PDB-708** is a high speed PIN photodiode integrated with a wide band differential output transimpedance amplifier. It is packaged in a TO-18, 6 leaded hermetic package. Options include, SMA, ST & FC type fiber optic ADMs.

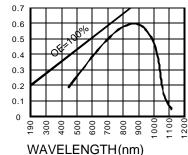
APPLICATIONS

- Fiber optic receivers
- Industrial controls
- High speed optical coupling
- Local area network

SPECTRALRESPONSE



DESCRIPTION



RESPONSIVITY (A/W)

*1/16 inch from case for 3 secs max

PHOTODIODE ELECTRO-OPTICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

SYMBOL	CHARACTERISTIC	TESTCONDITIONS	MIN	TYP	MAX	UNITS
lsc	Short Circuit Current	H = 100 fc, 2850 K	7	8.5		μA
ΙD	Dark Current	$H = 0, V_{R} = 10 V$		2	10	nA
Rsh	Shunt Resistance	$H = 0, V_{R} = 10 \text{ mV}$		500		MΩ
TC RSH	RSH Temp. Coefficient	$H = 0, V_{R} = 10 \text{ mV}$		-8		% / °C
CJ	Junction Capacitance	$H = 0, V_{R} = 45 V^{**}$		2.2	2.4	pF
λrange	Spectral Application Range	Spot Scan	400		1100	nm
λρ	Spectral Response - Peak	Spot Scan		900		nm
Vbr	Breakdown Voltage	I = 1 μA	100	300		V
NEP	Noise Equivalent Power	VR = 45 V @ Peak		1x10 ⁻¹⁴		W/√ ^{Hz}
tr	Response Time	$RL = 50 \Omega V_R = 45 V \lambda = 900 nm$		3		nS

Information in this technical data sheet is believed to be correct and reliable. However, no responsibility is assumed for possible inaccuracies or omission. Specifications are subject to change without notice. ** f = 1 MHz PAGE 1 OF 2 [FORM NO. 100-PDB-708 REV N/C]

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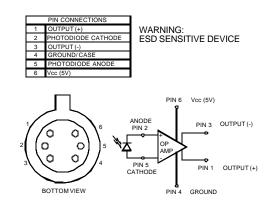
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 $\label{eq:amplifier} AMPLIFIER SPECIFICATION \quad (\text{so package } @\texttt{T}_{\texttt{A}} = 25^{\circ} \, \texttt{C} \, \texttt{and} \, \texttt{VS} = +5 \, \texttt{vdc} \, \texttt{UNLESS} \, \texttt{OTHERWISE} \, \texttt{NOTED}$

CHARACTERISTIC	TEST CONDITIONS	MIN	TYP	MAX	UNITS
DYNAMIC PERFORMANCE BANDWIDTH PULSE WIDTH MODULATION RISE AND FALL TIME SETTLING TIME	3 dB 10 µ A TO 200 µ A PEAK 10% TO 90% TO 3%, 0.5 V DIFF OUTPUT STEP	180	500 1.5 3		MHz ps ns ns
INPUT LINEAR INPUT CURRENT RANGE MAX INPUT CURRENT RANGE OPTICAL SENSITIVITY INPUT STRAY CAPACITANCE INPUT BIAS VOLTAGE	DIE, BY DESIGN SOIC, BY DESIGN +V _S TO I _N AND V _{BYP}	±200 1.6	±30 ±350 -36 0.2 0.4	2.0	μΑ μΑ dBm pF pF V
NOISE INPUT CURRENT NOISE TOTAL INPUT RMS NOISE	DIE, SINGLE ENDED AT P_{OUT} , OR DIFFERENTIAL ($P_{OUT} - N_{OUT}$), $C_{STRAY} = 0.3 \text{ pF}$ f = 100 MHz DC TO 100 MHz		3.0 26.5		pA/√Hz nA
TRANSFER CHARACTERISTICS TRANSRESISTANCE POWER SUPPLY REJECTION RATIO	SINGLE ENDED DIFFERENTIAL SINGLE ENDED DIFFERENTIAL	8 16	10 20 37.0 40	12 24	KΩ KΩ dB dB
OUTPUT DIFFERENTIAL OFFSET OUTPUT COMMON-MODE VOLTAGE VOLTAGE SWING (DIFFERENTIAL) OUTPUT IMPEDANCE	FROM POSITIVE SUPPLY POSITIVE INPUT CURRENT, $R_{L} = \infty$ POSITIVE INPUT CURRENT, $R_{L} = 50 \Omega$	-1.5 40	6 -1.3 1.0 600 50	20 -1.1 60	mV V V _{PP} MV _{PP} Ω
POWERSUPPLY OPERATING RANGE CURRENT	T _{MIN} TOT _{MAX} SINGLE SUPPLY DUAL SUPPLY	+4.5 ±2.25	+5 25	+11 ±5.5 26	V V mA

AMPLIFIER ABSOLUTE MAXIMUM RATING (TA=25 °CUNLESSOTHERWISENOTED)

PARAMETER	MIN	MAX	UNITS
SUPPLYVOLTAGE	±4.5	±12	V
POWER DISSIPATION		.9	μ V
STORAGETEMPERATURE	-55	+125	° C
OPERATINGTEMPERATURE	-40	+85	° C



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